



1374.39932CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: J. Noguchi et al.

Serial No.: 10/811,927

Filed: March 30, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
FABRICATION METHOD FOR SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

Art Unit: 2822

Examiner: Au, Bac

**AMENDMENT**

Mail Stop: Amendment  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

December 28, 2006

Sir:

This is in response to the Office Action mailed August 28, 2006, in connection with the above-identified application. The amendments are listed below and set forth on the following pages.

Amendments to the Claims; and

Remarks are included following the amendments.